IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	:	Gabriele Luculli
Application No.	:	10/666,725
Filed	:	September 19, 2003
For	:	GENERIC DEVELOPMENT TOOLS FOR EMBEDDED SOFTWARE DESIGN

Art Unit	:	2825
Docket No.	:	852263.411
Date	:	February 27, 2004

Mail Stop Missing Parts
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT
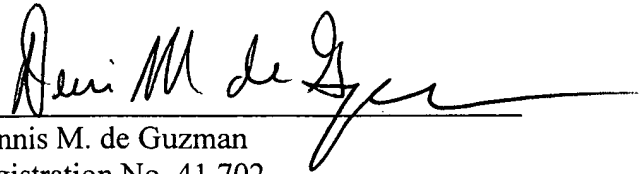
Commissioner for Patents:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicant wishes to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449 (copies of the cited references are enclosed). As to any reference supplied, applicant does not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and specifically reserves the right to traverse or antedate any such reference, as by a showing under 37 C.F.R. § 1.131 or other method. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicant's duty to disclose all information she is aware of which is believed relevant to the examination of the above-identified application, applicant believes that her invention is patentable.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Applicant believes this Information Disclosure Statement has been timely filed, however, the Commissioner is authorized to charge any fee due by way of this Information Disclosure Statement to our Deposit Account No. 19-1090.

Respectfully submitted,

Seed Intellectual Property Law Group PLLC

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Enclosures:
    Form PTO-1449
    Cited References (5)

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	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 852263.411	APPLICATION NO. 10/666,725
FORM PTO-1449 (REV. 3-80)		APPLICANT Gabriele Luculli	
FEB 27 2004 INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)		FILING DATE September 19, 2003	GROUP ART UNIT 2825

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
	AH					
	AI					
	AJ					

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AK	Hartoog, M. et al., "Generation of Software Tools from Processor Descriptions for Hardware/Software Codesign," *IEEE Proc. of 34th DAC*, June 1997.
	AL	Luculli, G., "An ISA-Retargetable Framework for Embedded Software Analysis," in *Proceedings of the 10th IEEE International Conference and Workshop on Engineering of Computer-Based Systems*, pp. 1-8, April 2, 2003.
	AM	Pees, S. et al., "Retargetable Compiled Simulation of Embedded Processors Using a Machine Description Language," *ACM Transactions on Design Automation of Electronic Systems* 5(4):815-834, October 2000.
	AN	Ramsey, N., "Pragmatic Aspects of Reusable Program Generators," *Journal of Functional Programming* 13(3):601-646, May 2003.
	AO	Ramsey, N. et al., "Specifying Representations of Machine Instructions," *ACM Transactions on Programming Language and Systems* 19(3): 492-524, May 1997.

EXAMINER	DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

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